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To:	USPTO	From:	Michelle Craig, Re	eg. No. 52,776

Please find attached for filing in connection with application no. 10/629,644, entitled PARALLEL CONVOLUTIONAL ENCODER, the following documents:

- REV/POA
- Statement Under 37 CFR 3.73(b)
- Copy of Assignment of Patent Rights executed 10/31/2005 (10 pages)
- Copy of Assignment of Patent Rights executed 12/23/2005 (16 pages)

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office on:
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PTO/SB/80 (04-05)
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POWER OF ATTORNEY TO PROSECUTE APPLICATIONS BEFORE THE USPTO

I hereby 37 CFR	revoke all p 3.73(b).	previous powers of attorney	given in the	spp	llcation Identifie	d in the a	attached state	ement under		
I hereby	appoint;						~ 			
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any and all	i patent appilca	to represent the undersigned before to the undersigned only to the undersigned cordance with 37 CFR 3.73(b).	ore the United S gned according	tates to th	Patent and Tradem e USPTO essignme	ark Office Int records	(USPTO) in cor or assignment o	mection with documents		
Please cha	ange the corres	pondence address for the applicat	fion identified in	the :	attached stalement i	under 37 C	FR 3.73(b) to:			
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Assignee N	lame and Addr	ess:					· · · · · · · · · · · · · · · · · · ·			
Assignee Name and Address: Zarbaña Digital Fund LLC										
Zarbaña Digital Fund LLC 2711 Centerville Road, Suite 400										
Wilmington, DE 19808										
A copy of this form, together with a statement under 37 CFR 3.73(b) (Form PTO/SB/96 or equivalent) is required to be filed in each application in which this form is used. The statement under 37 CFR 3.73(b) may be completed by one of										
me brace	tioners appo	sinted in this form if the appo	ointed practif	llone	r is authorized t	o act on	behalf of the	assignee,		
and must	loentity the	application in which this Po-					······			
<u>. </u>	The ind	SIGNAT ividual whose signature and title;	TURE of Assign is supplied bald	nea c Jw is	of Record authorized to act or	n behalf of	the assignee			
Signature	1	1				Date	1/13/06			
Name	Bryan B	urpee, Authorized Person	for Zarbaña	ı Diç	Ital Fund LLC	Telephor	-			
Tille				·			<u> </u>			
This collection	n of Information is	s required by 97 (IED 1 91 4 97 and 1	mt) 77% - 1 - 6 1	100 10						

This collection of information is required by 37 CFR 1.31, 1.32 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentially is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief-Information-Officer. U.S. Palent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT BEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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STATEMENT UNDER 37 CFR 3.73(b) Applicant/Patent Owner: Zarbaña Digital Fund LLC Application No./Patent No.: 10/629,644 Filed/Issue Date: 7/29/2003 Entitled: PARALLEL CONVOLUTIONAL ENCODER Zerbaña Digital Fund LLC _____, a Limited Liability Company (Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.) states that it is: 1. It the assignee of the entire right, title, and interest; or 2. an assignee of less than the entire right, title and interest. The extent (by percentage) of its ownership interest is _____% in the patent application/patent identified above by virtue of either. A. [] An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____ or for which a copy thereof is attached. OR B. [/] A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below. 1. From: Maher Amer To: Icefyre Semiconductor Corporation The document was recorded in the United States Patent and Trademark Office at Reel 014365 , Frame 0425 , or for which a copy thereof is attached. 2. From: Icefyre Semiconductor Corporation To: Icefyre Semiconductor, Inc. The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached. 3. From: Icefyre Semiconductor, Inc. To: Zarbaña Digital Fund LLC The document was recorded in the United States Patent and Trademark Office at Reel ______, or for which a copy thereof is attached. [] Additional documents in the chain of title are listed on a supplemental sheet. [v] Copies of assignments or other documents in the chain of title are attached. [NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08] The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee. -25-0W Michelle Craig, Reg. No. 52,776 Date yped ar printed name 503 439-650 Telephone number Signature Attorney at Law Title

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

PATENT ASSIGNMENT

IceFyre Semiconductor Corporation, c/o Fraser Milner Casgrain LLP, 99 Bank Street, Suite 1420, Ottawa, Ontario, K1P 1H4

(hereinafter "Assignor")

IceFyre Semiconductor, Inc., c/o Fraser Milner Casgrain LLP, 99 Bank Street, Suite 1420, Ottawa, Ontario, K1P 1H4

(hereinafter "Assignee")

WHEREAS the Assignor and Assignee executed the Intellectual Property Transfer Agreement dated October 3/, 2005, which provides for the purchase by Assignee of certain patent rights of Assignor, and

WHEREAS the Assignor, in consideration of CAD one dollar (\$1.00) and other good and valuable consideration, the receipt of which is hereby acknowledged, do hereby sell, assign and transfer to the Assignee and its successors and assigns:

- Assignor's entire right, title and interest in Canada, the United States and (i) throughout the world in and out to the patents and patent applications listed in Schedule A attached hereto, including any and all inventions described therein, and in any and all continuations-in-part, continuations, divisions, substitutes, reissues, re-examinations, or extensions thereof, and all other applications for patent relating thereto which have been filed, or hereafter shall be filed, in Canada, the United States or in any other jurisdiction and further including all rights under treaties to file and prosecute patent applications corresponding to the preceding patents and patent applications (the "Patents");
- (ii) all of Assignor's corresponding right, title and interest in and to any patents which may issue therefore, the same to be held and enjoyed by Assignee to the full end of the term for which the said patent is granted and maintained, as fully and entirely as the same could have been held and enjoyed by Assignor, and;
- the right to take action and recover in respect of any infringement of the Patents (iii)that took place prior to the date of this Assignment.

The Assignor hereby authorizes the issuance of any and all registrations for the Patents to the Assignee, its successors, assigns or legal representatives.

The Assignor hereby irrevocably designates and appoints the Assignee and its duly authorized officers and agents as the Assignor's agent and attorney in fact, to act for and on the Assignor's behalf and stead, to do all such lawful acts and things and to execute such further lawful assignments, documents, assurances, applications and other instruments as reasonably may be required by the Assignee, its successors, assigns or legal representatives, to obtain any and all

2

registrations for the Patents and to vest the same in the Assignee, its successors, assigns or legal representatives.

The Assignor hereby agrees to execute and sign all documents required to effect a recordation of the assignment of the Patents and registrations thereof before the proper office or agency.

The remainder of this page is intentionally left blank.

EXECUTED at: Toronto this 3 day of October, 2005 IceFyre Semiconductor Corporation	
Name: Jim Laird Title: Director	

I, Cott Cart	ENT OF WITNESS , whose full post office address is was
personally present and did see execute the above assignment.	Name: Name: Date: 3/it, 2005

COUNTERPART SIGNATURE PAGE TO PATENT ASSIGNMENT

Title		Status	Number	Published / Unpublished	Fees Current and Paid	Foreign Palents	Comments
	SWITCHED MODE POWER AMPLIFIER INTEGRALLY PERFORMING POWER COMBINING	lssued	6603352	Published	Yes	Yes. See Below.	panss
ם	ication	Nationalized					
SG	Japanese Nationalization	Pending	2003-550250	Published	Yes		Request for Examination due December 3, 2005.
Z	Korean Nationalization	Pending	7008505/2004	TBD	Yes		
ပ္က	Chinese Nationalization Pending	Pending	2824126.6	TBD	Yes		
SWITCHED POWER AN INTEGRAL! PERFORM!	SWITCHED-MODE POWER AMPLIFIER INTEGRALLY PERFORMING POWER COMBINING (CIP)	panss	937,096	Published	Хеs		ssued
SELECTAB INVERSION GAIN COM DIVERSITY RECEPTIO TRANSCEI	SELECTABLE INVERSION/VARIABLE GAIN COMBINER FOR DIVERSITY RECEPTION IN RF TRANSCEIVERS	Abandoned	10/068,120	Published	. ON	Yes. See Below.	Abandoned
일	PCT Application	Nationalized					
adiaı	Canadian Nationalization	Abandoned	2455111	Published	Yes		Abandoned
ese	Chinese Nationalization	Pending	2818192,1	Published	Yes		
рса	European Nalionalization	Pending	2748525.9	Published	Yes		Abandoned, but still revivable through 1/06.
Japanese	e Nationalization	Pending	2003-518082	Published	Yes		
an	Korean Nalionalization	Pending	7001206/2004	TBD	Yes		
Norwegian Nationaliza	Norwegian Nationalizallon	Pending	20040269	TBD	Yes		Abandoned, but still revivable through 1/06.
				-			•

Comments	Allowed. Issue Fee due about 1/20/03.	Abandoned			Abandoned						Abandoned			
Foreign Patents	Q B	A	PCT Application Pending.		PCT Application Pending.		Yes. See Below.					Yes. See Below.		
Fees Current	Yes	Yes	Yes	Yes	No	Yes	Yes		Yes	Yes	Yes	Yes		Yes
Published /	Allowed	Published	l²ublished	Published	Published	Published	Published		Not Published	Published	Published	Published	1	Not Published
Number	10/094,826	PCT/CA02/01498	10/154,282	PCT/CA02/01497	10/155,107	PCT/CA02/01499	10/273,908		TBD	10/377,859	PCT/CA2004/0002 82	10/629,644		78D
Stafus	Pending	Pending	Pending	Pending	Abandoned	Pending	Pending	Nationalized	Pending	Pending	Abandoned	Pending	Nationalized	Pending
	NOISE SSION/IMAGE DN UP AND		ON FOR ANSCEIVER	PCT Application	Oscillator Frequency Offsets	PCT Application	ION VD SIGNALS	PCT Application	Japanese Nationalization	MS AND ES FOR USE TRELLIS-BASED	PCT Application	LEII JLUTIONAL JER	PCT Application	Chinese Nationalization
Title	PSUEDO CARRIER SUPPRES REJECTION	PCT Application	UP/DOWN CONVERSI CIRCUITRY RADIO TRA	PCT App	Oscillato Offsets	PCT Ap	PHASOR FRAGMENTAT CIRCUITRY AN METHOD FOR PROCESSING MODULATED	PCT Ap	Japanes	SYSTEMS P MODULES F WITH TREL DECODING	PCT Ap	PARALLEI CONVOLU ENCODER	PCT Ap	Chinese
{em	ICE-003	ICE-003PC	ICE-004	ICE-004PC	ICE-005	ICE-005PC	ICE-006	ICE-006PC	ICE-006.JP	ICE-007	ICE-007PC	ICE-008	ICE-008PC	ICE-008CN

IceFyre Semiconductor Corp. Patent Related Information As of September 22, 2005

Comments				Abandoned		Abandoned			Response to Examination Report Due December 15, 2005.			Abandoned, but still revivable through	
Foreign Patents		Yes. See Below.		Yes. See Below.									
Fees Current and Paid	Yes	Yes	Yes	No		Yes	Yes		Yes	Yes	Yes	≺es	Yes
Published / Unpublished	Not Published	Published	Published	Published		TBD	Published		Published	Published	TBD	ТВО	Published
Number	2004-525088	10/629,640	PCT/CA03/01132	09/918,106		2,455,277	2818664.8		2748528.3	2003-518144	7001445/2004	20040367	10/205,743
Status	Pending	Pending	Pending	Abandoned	Nationalized	Pending	Pending	Nationalized	Pending	Pending	Pending	Pending	Pending
	Japanese Nationalization	PARALLEL SCRAMBLER/DESCRA MBLER		IONAL AND FOR NG ED SIGNALS ON-	ation	Canadian Nalionalization	ationalization	cation	European Nationalization	Nationalization	Korean Nationalization		AND AND S FOR SING IED SIGNALS ION- IT
Tille	Japanese h	PARALLEL SCRAMBLI MBLER	PCT Application	COMPUTATIO CIRCUITS AND METHODS FO PROCESSING MODULATED HAVING NON- CONSTANT ENVELOPES	PCT Application	Canadian	Chinese Nation	PCT Application	European	Japanese	Korean Na	Norwegian Nationaliza lon	COMPUTATIO CIRCUITS AND METHODS FO PROCESSING MODULATED HAVING NON- CONSTANT ENVELOPES (
llem	ICE-008JP	ICE-009	ICE-009PC	ICE-010	ICE-010PC	ICE-010CA		ICE-011PC	ICE-010EP	ICE-010JP	ICE-010KR	ICE-010NO	ICE-010CP

IceFyre Semiconductor Corp. Patent Related Information As of September 22, 2005

Comments	{ssued		Response to Examination Report Due December 17, 2005.					Nationalization Due: 12/30/05.	Allowed and ready for Issuance, Issue Fee Paid	Allowed. Issue Fee Due 12/23/05.
Foreign Patents	Yes. See Below.				·		PCT Application Pending.			
Fees Current and Paid	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes
Published / Unpublished		TBD	Published	Published	Published	Published	Published	Published	Issue Fee Paid	Allowed
Number	6,836,183	TBD	TBD	10/377,860	10/613,355	10/641,370	10/610,454	PCT/CA2004/0009 49	10/613,372	10/641,372
Status	ssued	Pending	Pending	Pending	Pending	Pending	Pending .	Pending	Allowed	Allowable
	RE	onalization	lion	YSTEMS OD FOR ELLIS- CODING		RTION OR A SYSTEM	Σ		ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM	ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM (CIP)
Title	CHIREIX ARCHITECTU USING LOW IMPEDANCE AMPLIFIERS	Japanese N	European N	MEMORY SYSTEMS AND METHOD FOR USE IN TRELLIS- BASED DECODING	PREDISTORTION CIRCUIT FOR A TRANSMIT SYSTEN	PREDISTORTION CIRCUIT FOR A TRANSMIT SYSTEM (CIP)	A METHOD OF AND DEVICE FOR ANTENNAE DIVERS SWITCHING	PCT Application	ADAPTIVE PREDISTO A TRANSM	ADAPTIVE PREDISTO A TRANSM (CIP)
Item	ICE-011	ICE-011JP	ICE-011EP	ICE-012	ICE-013	1CE-013CP	ICE-014	ICE-014PC	(CE-015	ICE-015CP

IceFyre Semiconductor Corp. Palent Related Information As of September 22, 2005

	- -		:					
<u>llem</u>	Title		Status	Number	Published / Unpublished	Fees Current and Paid	Foreign Patents	Comments
ICE-016	SWITCHED-MODE POWER AMPLIFIER USING LUMPED ELEMENT IMPEDANCE INVERTER FOR PARALLEL COMBINING	CE	lssued	6,879,209	Published	Yes		ssued
ICE-016C1	SWITCHED-MODE POWER AMPLIFIER USING LUMPED ELEMENT IMPEDAN INVERTER FOR PARALLEL COMBIN (CIP)	CE ING	Pending	11/099,916	Published	Yes	•	Final (6 Moulli) date to respond to office action: December 17, 2005.
ICE-017	INTEGRATED CIRCUIT INCORPORATING WIRE BOND INDUCTANCE		Pending	10/610,497	Published	Yes	•	Nolice of Appeal with Appeal Brief Due 12/30/05.
ICE-018	DIGITAL BRANCH CALIBRATOR FOR RF TRANSMITTER	RANCH OR FOR AN MITTER	Pending	10/627,881	Published	Yes		
ICE-0:19	ADAPTIVE PREDISTORTION FC A TRANSMIT SYSTE WITH GAIN, PHASE AND DELAY ADJUSTMENTS	ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM WITH GAIN, PHASE AND DELAY ADJUSTMENTS	Allowed	10/613,856	issue Fee Paid	Yes	PCT Application Pending.	Allowed and ready for Issuance. Issue fee paid. Checking status.
ICE-019PC	PCT Applica	:	Pending	CA/2004/000972	Published	Yes		
ICE-019CP1	ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEN WITH GAIN, PHASE AND DELAY ADJUSTMENTS (CIP)	ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM WITH GAIN, PHASE AND DELAY ADJUSTMENTS (CIP)	Allowable	10/641,371	Allowed	Yes		Allowed. Issue fee due November 22, 2005.

teeFyre Semiconductor Corp. Patent Related Information
As of September 22, 2005

<u>lleın</u>	Title	3)	Stalus	Number	Published / Unpublished	Fees Current and Paid	Foreign Patents	Comments
ICE-019CP2	ADAPTIVE PREDISTORTION A TRANSMIT SYS WITH GAIN, PHA AND DELAY ADJUSTMENTS (I FOR STEM SE	Allowable :	10/641,374		Yes		Allowed. Issue fee due December 6, 2005.
ICE-019CP3	ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM WITH GAIN, PHASE AND DELAY ADJUSTMENTS (CIP)		Allowable	10/641,373	Allowed	Yes		Allowed. Issue fee due November 24, 2005.
ICE-020	STAGGERED AGC WITH DIGITALLY CONTROLLED VGA		Pending	10/661,945	Published	Yes	PCT Application Filed.	
ICE-020PC	PCT Application OPTIMIZED FFT/IFFT MODULE	THEFT	Pending Pending	CA2004/001566 10/662,063	Published Published	Yes		
ICE-022	METHOD FOR AMPLITUDE INSENSITIVE DETECTION	ACKET	Pending	10/661,943	Published	Yes	PCT Application Filed.	
ICE-022PC	PCT Application	. ,	Pending	CA2004/001565	Published	Yes		
ICE-023	FREQUENCY DOM/ EQUALIZER FOR WIRELESS COMMUNICATIONS SYSTEM	Z Z	Pending	10/661,147	Published	Yes	PCT Application Filed.	
ICE-023PC	PCT Application		Pending	CA2004/001564	Published	Yes		
ICE-029	METHODS AND SYSTEMS FOR AMPLIFICATION THROUGH ENV REMOVAL AND RESTORATION	SIGNAL I ELOPE	Pending	10/779,322	Not Published	Yes	PCT Application Filed.	
ICE-029PC	PCT Application	tion	Pending	CA2005/000153	Not Published	Yes		
849099			•		•			

lceFyre Semiconductor Corp. Palent Related Information As of September 22, 2005

डी					nal on. ion due er 5, 2005.	
Comments					Provisional application. Conversion November 5	
Foreign Patents						
Fees Current and Paid		Yes	Yes	Yes	Yes	
Published / Unpublished	Not Published	Not Published	Not Published	Not Published	Not Published	
Number	10/883,170	10/884,633	10/954,429	10/884,627	60/625,301	
Status	Pending	Pending	Pending	Pending	Pending	
	RAPID ION TION	Multiple Input, Multiple Output Communications Systems	Multiple Input, Multiple Output Communications Systems (Continuation)		aled	
Title	SYSTEMS AND METHODS FOR I SIGNAL DETECT AND IDENTIFICA	Multiple Ing Output Cor Systems	Mulliple Inp Oulput Cor Systems (C	Power Amplifier	Improved Rower Amplifier and Rel Methods.	
Item	ICE-030	ICE-031	ICE-031C1	ICE-032	ICE-033PR	849099

Exhibit B

ASSIGNMENT OF PATENT RIGHTS

patents or patent applications to which any of the foregoing claim priority, and (c) current or future rights to (i) provisional patent any provisional patent application, patent application or patent listed below and all other rights arising out of such inventions and Delaware limited liability company, having an office at 2711 Centerville Road, Suite 400, Wilmington, New Castle Country, DE categories (a), (b), (c) and (d), including, without limitation, under the Paris Convention for the Protection of Industrial Property, protection, design patent protection, and other governmental grants; (d) the rights to all inventions and discoveries described in discoveries; (e) rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial applications, patent applications, and patents of any kind relating to any inventions and discoveries described in any provisional (whether currently pending, filed, or otherwise) and other enforcement rights, including, without limitation, all rights under the applications and patents listed below and/or under or on account of any of the foregoing For good and valuable consideration, the receipt of which is hereby acknowledged, IceFyre Semiconductor, Inc., a 19808 ("Assignee"), or its designees, all right, title and interest that exist today and may exist in the future in and to all of the the International Patent Cooperation Treaty, or any other convention, treaty, agreement or understanding; (f) causes of action counterparts to any of the foregoing, including, without limitation, certificates of invention, utility models, industrial design following (the "Patent Rights"): (a) the provisional patent applications, patent applications and patents listed below, (b) all continuations in part, continuing prosecution applications, and divisions of such patents and applications; and (iii) foreign design protections, design patent protections or other governmental grants of any type related to the any of the foregoing Delaware Corporation, ("Assignor"), does hereby sell, assign, transfer and convey unto Zarbaña Digital Fund LLC, a and patents listed below; (ii) reissues, reexaminations, extensions, continuations, patent applications, patent applications provisional patent applications, patent categories (b), (c) and/or (d) to

- damages,
- injunctive relief and Œ
- End other remedies of any ki

for past, current and future infringement; and

her payments under or on account of any of the foregoing (g) all rights to collect royalties and oth

	Title	Status	Number	Country	Inventor	Filing Date
Switched-Mode Power Amplifier Integrally Performing Power Combining	ing	Issued	6,603,352	U.S.A.	Wight, James	12/3/2001
Switched-Mode Power Amplifier Integrally Performing Power Combining	gu	Nationalized	CA02/01847	PCT	Wight, James	12/3/2002
Switched-Mode Power Amplifier Integrally Performing Power Combining	ρņ	Pending	2003-550250	Japan	Wight, James	12/3/2002
Switched-Mode Power Amplifier Integrally Performing Power Combining	60	Pending	7008505/2004	Korea	Wight, James	06/03/2004
Switched-Mode Power Amplifier Integrally Performing Power Combining	þΩ	Pending	2824126.6	China	Wight, James	12/3/2001
Switched-Mode Power Amplifier Integrally Performing Power Combining	50	Lapsed	2002351903	Australia	Wight, James	12/3/2002

Title	Status	Number	Country	Inventor	Filing Date
Switched-Mode Power Amplifier Integrally Performing Power Combining (CIP)	Issued 6,93	6,937,096	U.S.A.	Wight, James	6/30/2003
Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Expired 60/3(60/307/889	U.S.A.	Wight, James	7/27/01
Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Abandoned 10/0	10/068,120	U.S.A.	Wight, James	2/6/2.002
Reception Diversity Combiner with Selectable Inversion and Variable Gain	Nationalized CA02	CA02/01150	PCT	Wight, James	7/26/2002
Reception Diversity Combiner with Selectable Inversion and Variable Gain	Abandoned 24:	2455111	Canada	Wight, James	7/26/2002
Reception Diversity Combiner with Selectable Inversion and Variable Gain	Pending 281	2818192.1	China	Wight, James	7/26/2002
Reception Diversity Combiner	Dending 774	2748525.9	EPO	Wight, James	7/26/2002

ltem	Title	Status	Number	Country	Inventor	Filing Date
	with Selectable Inversion and Variable Gain					
ICE-002JP	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Abandoned	2003-518082	Japan	Wight, James	7/26/2002
ICE-002KR	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Pending	7001206/2004	Когеа	Wight, James	01/27/2004
ICE-002NO	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Abandoned but revivable	20040269	Norway	Wight, James	7/26/2002
ICE-003	Psuedo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Allowed	10/094,826	U.S.A.	Wight, James	3/11/2002
ICE-003PC	Psucdo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Expired	CA02/01498	PCT	Wight, James	10/4/2002
ICE-003AU**	Psuedo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Lapsed	2002328744	Australia	Wight, James	10/4/2002
ICE-004	Up/Down Conversion Circuitry for Radio Transceiver	Pending	10/154,282	U.S.A.	Birkett, Alexander	5/22/2002

Item	Title	Status	Number	Country	Inventor	Filing Date
ICE-004PC	Up/Down Conversion Circuitry for Radio Transceiver	Expired	CA02/01497	PCT	Birkett, Alexander	10/4/2002
ICE-004AU**	Up/Down Conversion Circuitry for Radio Transceiver	Lapsed	2002328743	Australia	Birkett, Alexander	10/4/2002
ICE-005	Oscillator Frequency Offsets	Abandoned	10/155,107	U.S.A.	Birkett, Alexander	5/23/2002
ICE-005PC	Frequency Offset Generator for Synthesized Signals	Expired	CA02/01499	PCT	Birkett, Alexander	10/4/02
ICE-005AU**	Frequency Offset Generator for Synthesized Signals	Lapsed	2002328745	Australia	Birkett, Alexander	10/4/2002
ICE-006	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Pending	10/273,908	U.S.A.	Parker, Kevin	10/18/2002
ICE-006JP	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Pending	2004-543858	Japan	Parker, Kevin	04/15/2005

Item	Title	Status	Number	Country	Inventor	Filing Date
ICE-006AU**	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Lapsed	2003278003	Australia	Parker, Kevin	10/14/2003
· ICE-006PC	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Expired	2004036862	PCT	Parker, Kevin	4/29/2004
ICE-007	Systems and Modules for Use with Trellis-Based Decoding	Pending	658'17'01	U.S.A.	Amer, Maher	2/28/2003
ICE-007PC	Viterbi Decoder Operating In Units Of a Plurality Of Transitions	Expired	CA04/000282	PCF	Amer, Maher	2/26/04
ICE-008PR	Parallel Convolutional Encoder	Expired	60/399,728	U.S.A.	Amer, Maher	8/1/2002
ICE-008	Parallel Convolutional Encoder	Pending	10/629,644	U.S.A.	Amer, Maher	7/29/2003
ICE-008KR	Parallel Convolutional Encoder	Pending	7001719/2005	Korea	Amer, Maher	01/31/2005
ICE-008CN	Parallel Convolutional Encoder	Pending	03818236.X	China	Amer, Maher	07/31/2003
ICE-008JP	Parallel Convolutional Encoder	Pending	2004-525088	Japan	Amer, Maher	03/24/2005

Item	Title	Status	Number	Country	Inventor	Dillian Det
ICE-008PC	Parallel Convolutional Encoder	Nationalized	CA03/0113	PCT	Amer, Maher	07/31/03
ICE-008AU**	Parallel Convolutional Encoder	Lapsed	2003249822	Australia	Amer, Maher	7/31/2003
ICE-009PR	Parallel Scrambler Descrambler	Expired	60/411,343	U.S.A.	Amer, Maher	9/18/02
ICE-009	Parallel Scrambler/Descrambler	Pending	10/629,640	U.S.A.	Amer, Maher	7/29/2003
ICE-009PC	Parallel Scrambler/Descrambler	Expired	CA03/01132	PCT	Amer, Maher	7/31/2003
ICE-009AU**	Parallel Scrambler/Descrambler	Lapsed	2003249821	Australia	Amer, Maher	7/31/2003
ICE-010PR	Processing Engines and RF Circuitry for Multi-Carrier Modulation Transceivers	Expired	60/277,941	U.S.A.	Wight, James	3/23/01

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Item	Title	Status	Number	Country	Inventor	Filing Date
	Non-Constant Envelopes		-			
ICE-010NO	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Abandoned but Revivable	20040367	Norway	Wight, James	1/27/2004
ICE-010CP	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes (CIP)	Pending	10/205,743	U.S.A.	Wight, James	. 7/26/2002
ICE-011	Chireix Architecture Using Low Impedance Amplifiers	Issued	6836183	U.S.A.	Wight, James	10/16/2002
ICE-011JP	Chireix Architecture Using Low Impedance Amplifiers	Pending	2004-543859	Јарап	Wight, James	04/15/2005
ICE-011PC	Chireix Architecture Using Low Impedance Amplifiers	Nationalized	CA03/001546	PCT	Wight, James	10/14/2003
ICE-011EP	Chireix Architecture Using Low Impedance Amplifiers	Pending	03769084	EPO	Wight, James	10/14/2003

Item	Title	Status	Number	Country	Inventor	Filing Date
ICE-011AU**	Chireix Architecture Using Low Impedance Amplifiers	Lapsed	2003278004	Australia	Wight, James	10/14/2003
ICE-012	Memory Systems and Method for Use In Trellis-Based Decoding	Pending	10/377,860	U.S.A.	Amer, Maher	2/28/2003
ICE-013	Predistortion Circuit for a Transmit System	Pending	10/613,355	U.S.A.	Saed, Aryan	7/3/2003
ICE-013CP	Predistortion Circuit for a Transmit System (CIP)	Pending	10/641,370	U.S.A.	Saed, Aryan	8/13/2003
ICE-014	A Method Of and Device for Antennae Diversity Switching	Pending	10/610,454	U.S.A.	Saed, Aryan	6/30/2003
ICE-014PC	A Method Of and Device for Receive Antennae Diversity Switching	Pending	CA04/000949	PCT	Saed, Aryan	6/23/04
ICE-015	Adaptive Predistortion for a Transmit System	Allowed	10/613,372	U.S.A.	Saed, Aryan	7/3/2003
ICE-015CP	Adaptive Predistortion for a Transmit System (CIP)	Allowed	10/641,372	U.S.A.	Saed, Aryan	8/13/2003

Item	Title	Status	Number	Country	Inventor	Filing Date
ICE-016	Switched-Mode Power Amplifier Using Lumped Element Impedance Inverter for Parallel Combining	Issued	6,879,209	U.S.A.	Grundingh, Johan	7/8/2003
ICE-016CP	Switched-Mode Power Amplifier Using Lumped Element Impedance Inverter for Parallel Combining (CIP)	Pending	11/099,916	U.S.A.	Grundingh, Johan	4/6/2005
ICE-017	Integrated Circuit Incorporating Wire Bond Inductance	Pending	10/610,497	U.S.A.	Wight, James	6/30/2003
ICE-018	Digital Branch Calibrator for An RF Transmitter	Pending	10/627,881	U.S.A.	Saed, Aryan	7/25/2003
ICE-019	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments	Allowed	10/613,856	U.S.A.	Saed, Aryan	7/3/2003
ICE-019PC	Adaptive Predistortion for a Transmit System with Gain,	Pending	CA04/000972	PCT	Saed, Aryan	6/30/2004

Item	Title	Status	Number	Country	Inventor	Filing Date
	Phase and Delay Adjustments					
ICE-019CP1.	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	Allowed	10/641,371	U.S.A.	Saed, Aryan	8/13/2003
ICE-019CP2	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	Allowed	10/641,374	U.S.A.	Saed, Aryan	8/13/2003
ICE-019CP3	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	Allowed	10/641,373	U.S.A.	Saed, Aryan	8/13/2003
ICE-020	Staggered AGC with Digitally Controlled VGA	Pending	10/661,945	U.S.A.	Birkett, Neil	9/12/2003
ICE-020PC	Staggered AGC with Digitally Controlled VGA	Pending	CA04/001566	PCT	Birkett, Neil	8/26/2004
ICE-021	Optimized FFT/IFFT Module	Pending	10/662,063	U.S.A.	Amer, Maher	9/12/2003
ICE-022	Method for Amplitude Insensitive Packet Detection	Pending	10/661,943	U.S.A.	Birkett, Neil	9/12/2003

Status
Pending
Systems and Methods for Rapid Signal Detection and Identification
Multiple Input, Multiple Output Communications Systems

ver Country	Status Number (
,429	Pending 10/954,429
,627	Pending 10/884,627
,301	Expired 60/325,301
yet ned	Pending assigned

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· By:

1900 Name:

Director Title:

(Signature MUST be notarized)

STATE OF MASSAM

COUNTY OF /////

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